

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

HAYASHI et al.

Application Number: 09/976,052

Filed: October 15, 2001

For: LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR
INTEGRATED CIRCUIT

Attorney Docket No. HITA.0110

Unit 2816

Examiner

Nguyen, Minh T

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

LETTER TO DRAFTSPERSON

Sir:

In response to the requirements set forth in the Office Action mailed on April 9, 2003, Applicants are submitting drawings for Figure 10, in which a legend of "Prior Art" is being added, in connection with the above-captioned application.

Kindly direct any inquiries in connection with this matter to the undersigned at the below-listed address and telephone number.

Respectfully submitted,

Stanley P. Fisher
Registration Number 24,344

Juan Carlos A. Marquez
Registration Number 34,072

REED SMITH LLP
3110 Fairview Park Dr., Suite 1400
Falls Church, Virginia 22042
(703) 641-4200

August 8, 2003

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